HP2-T16 ^{Q&As}

Industry Standard Architecture and Technology

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QUESTION 1

Which categories of questions should you ask the customer during needs analysis? (Select two) A. facility size

B. company location

C. future plans

D. business requirements

Correct Answer: CD

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\viewkind4\uc1\pard\lang2052\f0\fs17 Industry Standard Architecture - Student Guide 2 - Page 98\par Conducting a
needs analysis\par Future plans\par What are the business goals?\par What is the projected role of the server?\par
What is the projected operating system?\par Will RAID be implemented?\par Will the server be connected to more than
one network?\par Current environment\par How much storage currently is used?\par Have storage needs grown over
the last 12 months?\par Business requirements\par What is the expected availability of the server?\par Is server price or
functionality more important?\par Is a rack or tower configuration preferred?\par Will backups be performed?\par Is
power protection needed?\par What kinds of system management tools are needed?\par }

QUESTION 2

A customer has a single-threaded application running on a dual-core base ProLiant server. Processor utilization is
consistently between 80 - 100%. Which step should you take to reduce system load on the processors?

A. Replace with slower quad-core processors.

B. Replace with faster single core processors.

C. Change the application to use a single thread.

D. Add more memory.

Correct Answer: B

QUESTION 3

Which type of information is obtained during the site survey? (Select two)

A. biggest IT problem today

B. capacity of electrical circuits

C. projected role of the server

D. facility size

Correct Answer: BD

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\viewkind4\uc1\pard\lang2052\f0\fs17 Industry Standard Architecture - Student Guide 2 - Page 99:\par Conducting a site survey\par To evaluate these factors, you can use survey questions to gather data, as demonstrated by the following examples:\par How large is the facility?\par Does the facility currently have any radio frequency interference (RFI) problems?\par Is there any extra space?\par Will an existing space need to be modified?\par Are adequate utility outlets available in the proposed space?\par Are the electrical circuits of sufficient capacity?\par }

QUESTION 4

Which Linux utility helps you obtain information about I/O and processor statistics?

A. iostat

B. ioutil

C. vmstat

D. cpuinfo

Correct Answer: A

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\viewkind4\uc1\pard\lang2052\f0\fs17 Industry Standard Architecture - Student Guide 2 - Page 219\par iostat
---Measures I/O and processor statistics for devices and partitions.\par }

QUESTION 5

What is the most commonly used measurement unit for describing a UPS?

A. kW

B. VA

C. Amps

D. kJ

Correct Answer: B

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\viewkind4\uc1\pard\lang2052\f0\fs17 Industry Standard Architecture - Student Guide 2 - Page 187\par An uninterruptible power supply (UPS) system provides power to the server in case of loss of electrical power from the main building power. The UPS is rated in volt-amps (VA) which is the total power it can handle and the time it can run the server, usually the time required for the operating system to close all running applications, gracefully shut itself down, and turn off the server.\par }

QUESTION 6

Which server filters outgoing network requests?

A. FTP

B. Proxy

C. DNS

D. WINS

Correct Answer: B

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\viewkind4\uc1\pard\lang2052\f0\fs17 Industry Standard Architecture - Student Guide 1 - Page 17:\par Server functions
Proxy server ?Filters outgoing network requests\par }

QUESTION 7

How many address lines does an Intel Xeon processor use, and what is the maximum amount of accessible,
addressable memory?

A. 32 address lines; 64GB addressable memory

B. 36 address lines; 4GB addressable memory

C. 36 address lines; 64GB addressable memory

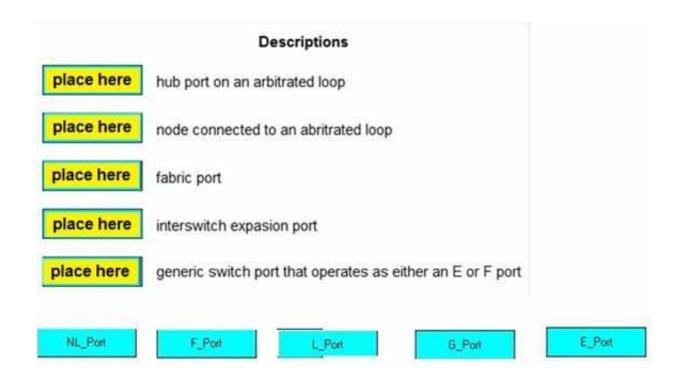D. 64 address lines; 64GB addressable memory

Correct Answer: C

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\viewkind4\uc1\pard\lang2052\f0\fs17 Advantages of EM64T\par 64-bit allows an installation of up to 16 EB (exabyte) of
RAM; however, current Celeron D, Pentium 4, and Xeon CPUs have 36 address lines, which can support 64 GB of RAM
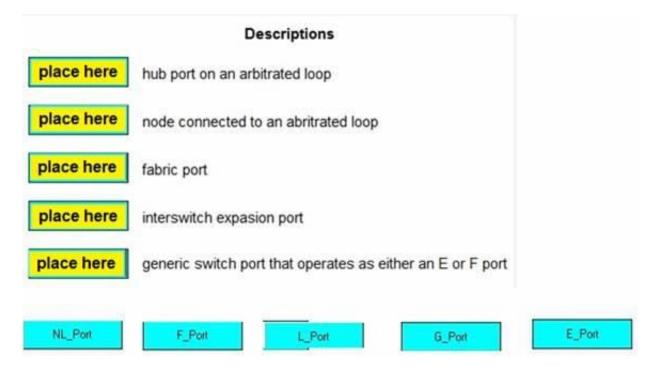, while Xeon DP CPUs can hold up to 1 TB (terabyte)\par }

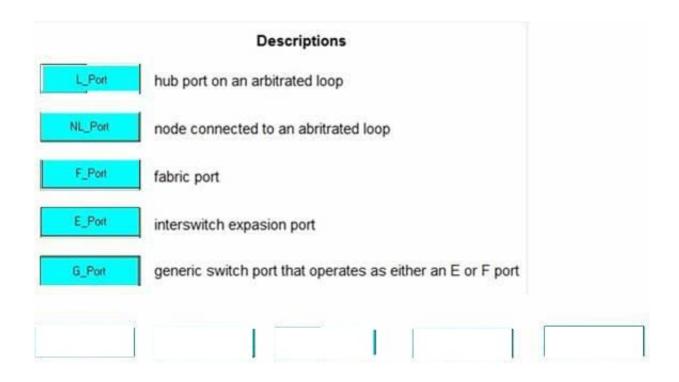**QUESTION 8**

Place each term next to its description

Select and Place:

Descriptions

place here	hub port on an arbitrated loop
place here	node connected to an abritrated loop
place here	fabric port
place here	interswitch expasion port
place here	generic switch port that operates as either an E or F port

NL_Port	F_Port	L_Port	G_Port	E_Port

Select and Place:

Descriptions

place here	hub port on an arbitrated loop
place here	node connected to an abritrated loop
place here	fabric port
place here	interswitch expasion port
place here	generic switch port that operates as either an E or F port

NL_Port	F_Port	L_Port	G_Port	E_Port

Correct Answer:

Descriptions	
L_Port	hub port on an arbitrated loop
NL_Port	node connected to an abritrated loop
F_Port	fabric port
E_Port	interswitch expasion port
G_Port	generic switch port that operates as either an E or F port

QUESTION 9

Which material should be used for cleaning the ends of fibre optic cables?

A. water

B. carbon dioxide

C. polyester cloth

D. gravel

Correct Answer: C

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\viewkind4\uc1\pard\lang2052\f0\fs17 Industry Standard Architecture - Student Guide 2 - Page 129\par Cabling best
practices For cleaning the ends of fiber-optic cables, be sure to use the proper material, which is polyester cloth.\par }

QUESTION 10

Which network management protocols are typically supported by system monitoring agents? (Select two)

A. Internet Message Access Protocol (IMAP)

B. Server Message Block (SMB)

C. Secure Sock Layer (SSL)

D. Intelligent Platform Management Interface (IPMI)

E. Web-based Enterprise Management (WBEM)

Correct Answer: DE

{\rtf1\ansi\ansicpg936\deff0\deflang1033\deflangfe2052{\fonttbl{\f0\fnil\fcharset0 MS Shell Dlg 2;}} \viewkind4\uc1\pard\lang2052\f0\fs17 Industry Standard Architecture - Student Guide 2 - Page 185\par Upgrading system monitoring agents \par Web-based Enterprise\par Management /\par Common Information\par Model (WBEM/CIM))\par The DMTF has recently replaced DMI with the Web-based Enterprise Management (WBEM) and Common Information Model (CIM) standards for system management. WBEM/CIM defines an object- based, client/ server model. In the WBEM/ CIM model, clients send requests for data about managed devices to the CIM object manager server which forwards those requests to providers for the specific devices. The providers return the requested data to the clients through the CIM object manager. The clients can also subscribe for indications about events that occur in the system.\par Intelligent Platform\par Management Interface (IPMI)\par IPMI is an embedded management specification for servers, storage devices, and other network devices. It defines a common and secure interface for monitoring system voltages, temperature, and fan speeds through the use of embedded monitors. It is designed to directly control system components while permitting remote system management and recovery of failed systems.\par }

QUESTION 11

What are two types of backbone SAN topologies? (Select two)

A. fat tree

B. meshed

C. skinny tree

D. cascaded

E. ring

Correct Answer: AC

{\rtf1\ansi\ansicpg936\deff0\deflang1033\deflangfe2052{\fonttbl{\f0\fnil\fcharset0 MS Shell Dlg 2;}} \viewkind4\uc1\pard\lang2052\f0\fs17 Industry Standard Architecture - Student Guide 1 - Page 295\par Fat trees and skinny trees are two types of backbone SAN topologies. The main difference between fat and skinny trees is the number of ISLs used to connect the edge switches to the backbone switches. The number of ISLs subtracts from the number of end ports and affects the total number of switches needed for a particular configuration. Fat trees use half the number of edge switch ports as ISL connections; skinny trees use less than half.\par }

QUESTION 12

Which statements are true about AMD 2P or 4P system architecture? (Select three)

A. Requests for memory access are handle by the Northbridge ASIC.

B. Memory must be installed in banks corresponding to the installed processors.

C. Each processor has its own memory controller

D. The maximum amount of memory can be installed, regardless of the number of installed processors.

E. Requests for memory access are handle directly by the corresponding processor and relayed through the

HyperTransport link.

F. Communications between CPU and memory is handle through the QuickPath Interconnect.

Correct Answer: BCE

QUESTION 13

What can you do to optimize memory performance?

A. Enable Advanced Memory Buffer.

B. Rearrange existing memory to allow interleaving.

C. Implement memory caching

D. Configure processor interleaving.

Correct Answer: B

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\viewkind4\uc1\pard\lang2052\f0\fs17 Industry Standard Architecture - Student Guide 1 - Page 124:\par Bank
interleaving\par SDRAM divides memory into two to four banks for simultaneous access to more data.\par This division
and simultaneous access is known as interleaving.\par In two-way interleaving while one memory bank is being
accessed, the other bank remains ready to be accessed. Thus the processor can initiate a new memory access before
the previous access has been completed, resulting in continuous data flow and increasing the amount of data accessed
in a single memory access. \par When data is written to memory, the memory controller distributes the data across
DIMMs in a bank. When the processor sends a read request to the memory controller, the memory controller sends the
request to all DIMMs in the bank simultaneously. The data at the requested address is returned along with data from
subsequent sequential addresses. The memory controller interleaves the data from all the DIMMs to put it back in its
original order.\par Because more than one DIMM is used in this transaction, the amount of data that can be written or
read is larger than if a single DIMM were used. For example, in dual-interleaved memory, where two DIMMs are used,
the processor can read and write twice the amount of data in one memory access. In four-way interleaved memory, the
processor can read and write four times the amount of data in one memory access.\par }

QUESTION 14

What factors are important to sustain the I/O rates of the application within an array? (Select two)

A. capacity of the disks within the array

B. speed of the disks within the array

C. number of disks within the array

D. location of the disks within an enclosure

Correct Answer: BC

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\viewkind4\uc1\pard\lang2052\f0\fs17 Industry Standard Architecture - Student Guide 1 - Page 249:\par Number of disks
in an array\par The RAID level and the number of drives affect the available I/O bandwidth for a given disk configuration.
You must have enough drives (regardless of the drive capacity) to sustain the I/O rates of the application. \par }

QUESTION 15

You are comparing similar versions of Intel Xeon and AMD Opteron processors. Which statements are true about these processors? (Select two)

A. Opteron processors use a Northbridge that operates at core bus speed.

B. AMD Opteron processors are optimized for virtualization and AMD-V technology.

C. An Intel Xeon processor uses HyperTransport link to access its memory.

D. Intel Xeon processors are optimized for virtualization with Intel VT technology.

E. An AMD Opteron processors uses QuickPath Interconnect to access its memory.

Correct Answer: BD

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\viewkind4\uc1\pard\lang2052\f0\fs17 Industry Standard Architecture - Student Guide 1 - Page 86:\par AMD-V and VT
virtualization technology \par Hardware-assisted virtualization technology is available from Intel (Intel VT) for Intel
EM64T processors and from AMD (AMD-V) for Opteron processors.\par }

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